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Distributed-directory scheme: scalable coherent interface

James, D.V., Laundrie, A.T., Gjessing, S., Sohi, G.S.

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This paper appears in: Computer

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On page(s): 74 - 77

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Abstract

The scalable coherent interface (SCI), a local or extended computer backplane interface being defined IEEE standard project (P1596), is discussed. The interconnection is scalable, meaning that up to 64 processor, memory, or I/O nodes can effectively interface to a shared SCI interconnection. The SCI structures are described, and sharing-list addition and removal are examined. Optimizations being considered to improve the performance of large system configurations are discussed. Request combining, a user of linked-list coherence, is described. SCI's optional extensions, including synchronization using a queue lock bit, are considered.

Index Terms

Inspec

[Controlled Indexing](#)[computer interfaces](#) [data structures](#)[Non-controlled Indexing](#)[IEEE standard project P1596](#) [SCI sharing-list structures](#) [computer backplane interface](#)
[distributed directory scheme](#) [large system configurations](#) [linked-list coherence](#) [queued-connection](#)
[lock bit](#) [scalable coherent interface](#)

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